

**AMENDMENTS TO THE CLAIMS**

Please amend claims 4, 9, 19, 22, 24, 26 and 28 as follows:

1. (Original) A method of decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said method comprising the steps of:

(a) weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured;

(b) carrying out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data; and

(c) compensating for results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

2. (Original) The method as set forth in claim 1, wherein said step (a) is carried out in each of slot periods.

3. (Original) The method as set forth in claim 1, wherein said step (a) includes a step of multiplying X with said reverse-diffused, received data, said X being defined as a value which is in proportion to a value obtained by dividing a root of a signal power per a slot by an interference power per a slot.

4. (Currently Amended) The method as set forth in claim 1, wherein said comparison/selection operation is carried out by means of a subtracting circuit, and said results of said ACS operation are compensated for by means of a logic circuit which receives an output or an absolute value of an ~~output transmitted from output~~ of said subtracting circuit, and outputs a predetermined value in accordance with said output.

5. (Original) The method as set forth in claim 1, wherein said step (a) is carried out through a firmware defined by a processor for processing digital signals, and said step (c) is carried out through a hardware including a logic gate.

6. (Original) A method of decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio, said method comprising the steps of:

(a) weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured;

(b) carrying out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data; and

(c) compensating for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

7. (Original) The method as set forth in claim 6, wherein said step (a) is carried out in each of slot periods.

8. (Original) The method as set forth in claim 6, wherein said step (a) includes a step of multiplying X with said reverse-diffused, received data, said X being defined as a value which is in proportion to a value obtained by dividing a root of a signal power per a slot by an interference power per a slot.

9. (Currently Amended) The method as set forth in claim 6, wherein said comparison/selection operation is carried out by means of a subtracting circuit, and said results of said ACS operation are compensated for by means of a logic circuit which receives an output or an absolute value of an ~~output transmitted from~~ output of said subtracting circuit, and outputs a predetermined value in accordance with said output.

10. (Original) The method as set forth in claim 6, wherein said step (a) is carried out through a firmware defined by a processor for processing digital signals, and said step (c) is carried out through a hardware including a logic gate.

11. (Original) A receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

12. (Original) The receiver as set forth in claim 11, wherein said power controller carries out weighting reverse-diffused, received data in each of slot periods.

13. (Original) The receiver as set forth in claim 11, wherein said power controller multiplies X with said reverse-diffused, received data, said X being defined as a value which is in proportion to a value obtained by dividing a root of a signal power per a slot by an interference power per a slot.

14. (Currently Amended) The receiver as set forth in claim 11, wherein said turbo decoder is comprised of:

(b1) a subtracting circuit which carries out said comparison/selection operation; and

(b2) a logic circuit which compensates for said results of said ACS operation, said logic circuit receiving an output or an absolute value of an ~~output transmitted from output of~~ said subtracting circuit, and outputting a predetermined value in accordance with said output

15. (Original) The receiver as set forth in claim 11, wherein said power controller weights reverse-diffused, received data through a firmware defined by a processor for processing digital signals, and said turbo decoder compensates for said results of said ACS operation through a hardware including a logic gate.

16. (Original) A receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

17. (Original) The receiver as set forth in claim 16, wherein said power controller carries out weighting reverse-diffused, received data in each of slot periods.

18. (Original) The receiver as set forth in claim 16, wherein said power controller multiplies X with said reverse-diffused, received data, said X being defined as a

value which is in proportion to a value obtained by dividing a root of a signal power per a slot by an interference power per a slot.

19. (Currently amended) The receiver as set forth in claim 16, wherein said turbo decoder is comprised of:

(b1) a subtracting circuit which carries out said comparison/selection operation; and

(b2) a logic circuit which compensates for said results of said ACS operation, said logic circuit receiving an output or an absolute value of an ~~output transmitted from output of~~ said subtracting circuit, and outputting a predetermined value in accordance with said output

20. (Original) The receiver as set forth in claim 16, wherein said power controller weights reverse-diffused, received data through a firmware defined by a processor for processing digital signals, and said turbo decoder compensates for said results of said ACS operation through a hardware including a logic gate.

21. (Original) A combination of a turbo encoder for turbo-encoding data to be transmitted, and a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said turbo encoder comprising a plurality of component encoders arranged in parallel with one another,

said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for

results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

22. (Currently Amended) The combination as set forth in claim 21, wherein said turbo encoder includes:

- (a) first to N-th component encoders each of which receives ~~data series information sequence~~ to be encoded, wherein N is an integer equal to or greater than 2;
- (b) an interleaver connected to said component encoders in parallel to rearrange said ~~data series information sequence~~ in accordance with a predetermined rule, said interleaver receiving said ~~data series information sequence~~ and transmitting said ~~data series information sequence~~ to said second to N-th component encoders; and
- (c) a switch which switches ~~parity series parity sequence~~ transmitted said component encoders.

23. (Original) A combination of a turbo encoder for turbo-encoding data to be transmitted, and a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said turbo encoder comprising a plurality of component encoders arranged in parallel with one another,

said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

24. (Currently Amended) The combination as set forth in claim 23, wherein said turbo encoder includes:

- (a) first to N-th component encoders each of which receives ~~data-series information sequence~~ to be encoded, wherein N is an integer equal to or greater than 2;
- (b) an interleaver connected to said component encoders in parallel to rearrange said ~~data-series information sequence~~ in accordance with a predetermined rule, said interleaver receiving said ~~data-series information sequence~~ and transmitting said ~~data-series information sequence~~ to said second to N-th component encoders; and
- (c) a switch which switches ~~parity-series parity sequence~~ transmitted said component encoders

25. (Original) A combination of a turbo encoder for turbo-encoding data to be transmitted, and a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said turbo encoder comprising a plurality of component encoders arranged in series, said receiver comprising:

- (a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and
- (b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

26. (Currently Amended) The combination as set forth in claim 25, wherein said turbo encoder includes:

- (a) an external encoder which receives ~~data-series information sequence~~ to be encoded;

(b) a puncturing circuit which punctures ~~data series information sequence~~ and ~~parity series parity sequence~~ both transmitted from said external encoder;

(c) an interleaver which rearranges bit arrangement in said ~~data series information sequence~~ and said ~~parity series parity sequence~~ each in accordance with a predetermined rule; and

(d) an internal encoder which receives said ~~data series information sequence~~ from said interleaver, and separates said ~~data series information sequence~~ and said ~~parity series parity sequence~~ from each other.

27. (Original) A combination of a turbo encoder for turbo-encoding data to be transmitted, and a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said turbo encoder comprising a plurality of component encoders arranged in series, said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

28. (Currently Amended) The combination as set forth in claim 27, wherein said turbo encoder includes:

(a) an external encoder which receives ~~data series information sequence~~ to be encoded;

(b) a puncturing circuit which punctures ~~data-series information sequence~~ and ~~parity series parity sequence~~ both transmitted from said external encoder;

(c) an interleaver which rearranges bit arrangement in said ~~data-series information sequence~~ and said ~~parity series parity sequence~~ each in accordance with a predetermined rule; and

(d) an internal encoder which receives said ~~data-series information sequence~~ from said interleaver, and separates said ~~data-series information sequence~~ and said ~~parity-series parity sequence~~ from each other.

29. (Original) A recording medium readable by a computer, storing a program therein for causing a computer to carry out a method of decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said method comprising the steps of:

(a) weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured;

(b) carrying out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data; and

(c) compensating for results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

30. (Original) A recording medium readable by a computer, storing a program therein for causing a computer to carry out a method of decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio,

said method comprising the steps of:

(a) weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured;

(b) carrying out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data; and

(c) compensating for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

31. (Original) A recording medium readable by a computer, storing a program therein for causing a computer to act as a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio, said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.

32. (Original) A recording medium readable by a computer, storing a program therein for causing a computer to act as a receiver for decoding turbo-encoded, received data in CDMA (Code Division Multiple Access) system which carries out closed-loop control to electric power of a data transmitter, based on a signal to interference ratio, said receiver comprising:

(a) a power controller for weighting reverse-diffused, received data, based on both said signal to interference ratio and data obtained when said signal to interference ratio is measured; and

(b) a turbo decoder which carries out ACS operation or comparison/selection operation in a process of updating alpha metric, a process of updating beta metric, and a process for computing likelihood, to the thus weighted, received data, and compensates for results of said ACS operation in at least one of said process of updating alpha metric, said process of updating beta metric, and said process for computing likelihood, based on a predetermined value associated with a difference generated when said ACS operation or said comparison/selection operation is carried out.